Product Specification

- () **Product Information**
- () Preliminary Specification
- (\checkmark) Approval Specification

Any modification of Spec is not allowed without SDC's permission.

CUSTOMER	R/A Customer		MODEL NO.	RS430ENT-	
DATE OF ISSUE	2019/011/1		EXTENSION CODE	-V(0)	

Customer Approval & Feedback	
	Customer Approval & Feedback

Approved by	ToNINO_
Prepared by	Luo Ciandrin

Product Specification

SPECIFICATION FOR APPROVAL

() Preliminary Specification
 () Final Specification

This specification is applicable to 43 "1500cd/m²

If there is any change to the specific panel information, we will inform you

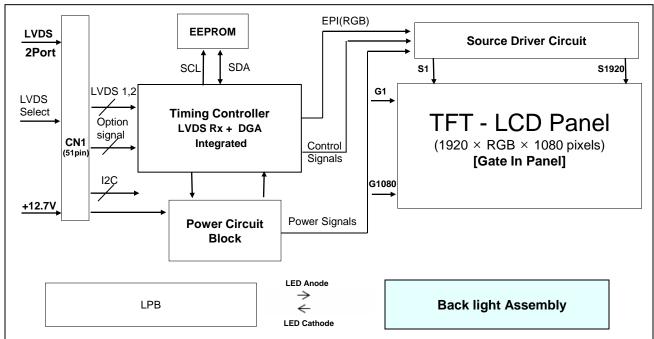
1. General Description

The IS430ENT is a Color Active Matrix Liquid Crystal Display with an integral Light Emitting Diode (LED) backlight system. The matrix employs a-Si Thin Film Transistor as the active element.

It is a transmissive display type which is operating in the normally black mode. It has a 43.0 inch diagonally measured active display area with WUXGA resolution (1080 vertical by 1920 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arrayed in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 8-bit gray scale signal for each dot. Therefore, it can present a palette of more than 16.7Milion colors.

It has been designed to apply the 8-bit 2-port LVDS interface.

It is intended to support Commercial Display where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



General Features

Active Screen Size	42.51inches (1079.87 mm) diagonal
Outline Dimension	973.8(H) x 567.0(V) x 40.0(D) (Typ.)58.0(D) (Max) mm
Pixel Pitch	0.4902 mm x 0.4902 mm
Pixel Format	1920 horiz. by 1080 vert. Pixels, RGB stripe arrangement
Color Depth	8bit, 16.7 Million colors
Luminance, White	1500cd/m ² (Center 1point ,Typ.)
Viewing Angle (CR>10)	Viewing angle free (R/L 178 (Min.), U/D 178 (Min.))
TotalPower Consumption	1500cd/m ² =78W
Weight	11.25 kg(Typ.) 11.5 kg (Max)
Display Mode	Transmissive mode, Normally black
Surface Treatment	Hard coating(3H), Anti-glare treatment of the front polarizer (Haze 1%(Typ.))
Possible Display Type	Landscape and Portrait Enabled

2. Absolute Maximum Ratings

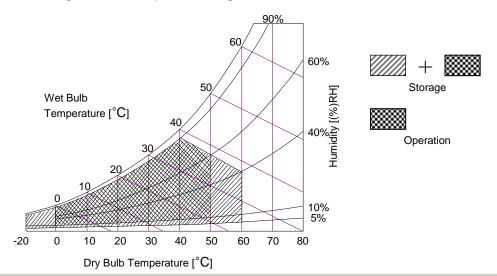
The following items are maximum values which, if exceeded, may cause faulty operation or permanent damage to the LCD module.

Table 1. ABSOLUTE MAXIMUM RATINGS (Only Panel and Circuit Part of LCD Module)

Para	Parameter		Va	lue	Unit	Note
Faia			Min	Max	Onic	Note
Power Input Voltage	LCD Circuit	VLCD	-0.3	+14.0	VDC	
LED Input Voltage Forward Voltage		VF	-	+130.5	VDC	1
T-Con Option Selection	T-Con Option Selection Voltage		-0.3	+4.0	Vdc	
Operating Temperature		Тор	0	+50	°C	2.2
Storage Temperature	Storage Temperature		-20	+60	°C	2,3
Panel Front Temperature		TSUR	-	+68	°C	4
Operating Ambient Humidity		Нор	10	90	%RH	
Storage Humidity		Нѕт	5	90	%RH	2,3

1. Ambient temperature condition (Ta = 25 ± 2 °C)

- 2. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be Max 39°C, and no condensation of water.
- 3. Gravity mura can be guaranteed below 40°C condition.
- 4. The maximum operating temperatures is based on the test condition that the surface temperature of display area is less than or equal to 68°C with LCD module alone in a temperature controlled chamber. Thermal management should be considered in final product design to prevent the surface temperature of display area from being over 68°C. The range of operating temperature may be degraded in case of improper thermal management in final product design.



3. Electrical Specifications

3-1. Electrical Characteristics

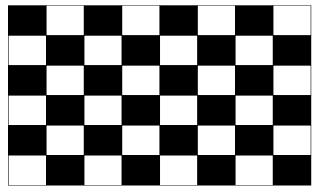
It requires two power inputs. One is employed to power for the LCD circuit. The other Is used for the LED backlight and LED Driver circuit.

Table 2.	ELECTRICAL	CHARACTERISTICS
----------	------------	------------------------

Parameter		Symbol		Value	Unit	Note	
		Symbol	Min	Тур	Max	Unit	Note
Circuit :							
Power Input Voltage	Power Input Voltage		10.8[TBD]	12.7	14.0	Vdc	
Power Input Current	Power Input Current		-	350	455	mA	1
		ILCD	-	550	715	mA	2
T-CON Option	Input High Voltage	V _{IH}	1.62	-	1.98	VDC	
Selection Voltage	Input Low Voltage	V _{IL}	0	-	0.54	VDC	
Power Consumption		PLCD	-	4.2	5.5	Watt	1
Rush current		Irush	-	-	5.0	А	3

notes

- 1. The specified current and power consumption are under the V_{LCD}=12.0V, Ta=25 \pm 2°C, f_V=60Hz condition, and mosaic pattern(8 x 6) is displayed and f_V is the frame frequency.
- 2. The current is specified at the maximum current pattern.
- 3. The duration of rush current is about 2ms and rising time of power input is 0.5ms (min.).
- 4. Ripple voltage level is recommended under $\pm 5\%$ of typical voltage.



White : 255 Gray Black : 0 Gray

Mosaic Pattern(8 x 6)

3-2. Interface Connections

This LCD module employs three kinds of interface connection, 51-pin connector is used for the module electronics.

3-2-1. LCD Module

- LCD Connector(CN1): FI-RXE51S-HF(manufactured by JAE) or compatible
- Mating Connector : FI-R51HL(JAE) or compatible

Table 4. MODULE CONNECTOR(CN1) PIN CONFIGURATION

No	Symbol	Description	No Symbol		Description
1	NC or GND	No Connection or Ground (Note 4)	27	NC	No connection
2	NC	No Connection (Note 4)	28	R2AN	SECOND LVDS Receiver Signal (A-)
3	NC	No Connection (Note 4)	29	R2AP	SECOND LVDS Receiver Signal (A+)
4	NC	No Connection (Note 4)	30	R2BN	SECOND LVDS Receiver Signal (B-)
5	NC	No Connection (Note 4)	31	R2BP	SECOND LVDS Receiver Signal (B+)
6	NC	No Connection (Note 4)	32	R2CN	SECOND LVDS Receiver Signal (C-)
7	LVDS Select	'H' =JEIDA , 'L' or NC = VESA	33	R2CP	SECOND LVDS Receiver Signal (C+)
8	NC	No Connection (Note 4)	34	GND	Ground
9	NC	No Connection (Note 4)	35	R2CLKN	SECOND LVDS Receiver Clock Signal(-)
10	NC	No Connection (Note 4)	36	R2CLKP	SECOND LVDS Receiver Clock Signal(+)
11	GND	Ground	37	GND	Ground
12	R1AN	FIRST LVDS Receiver Signal (A-)	38	R2DN	SECOND LVDS Receiver Signal (D-)
13	R1AP	FIRST LVDS Receiver Signal (A+)	39	R2DP	SECOND LVDS Receiver Signal (D+)
14	R1BN	FIRST LVDS Receiver Signal (B-)	40	NC	No connection
15	R1BP	FIRST LVDS Receiver Signal (B+)	41	NC	No connection
16	R1CN	FIRST LVDS Receiver Signal (C-)	42	NC or GND	No Connection or Ground
17	R1CP	FIRST LVDS Receiver Signal (C+)	43	NC or GND	No Connection or Ground
18	GND	Ground	44	GND	Ground (Note 5)
19	R1CLKN	FIRST LVDS Receiver Clock Signal(-)	45	GND	Ground
20	R1CLKP	FIRST LVDS Receiver Clock Signal(+)	46	GND	Ground
21	GND	Ground	47	NC	No connection
22	R1DN	FIRST LVDS Receiver Signal (D-)	48	VLCD	Power Supply +12.0V
23	R1DP	FIRST LVDS Receiver Signal (D+)	49	VLCD	Power Supply +12.0V
24	NC	No connection	50	VLCD	Power Supply +12.0V
25	NC	No connection	51	VLCD	Power Supply +12.0V
26	NC or GND	No Connection or Ground	-	-	-

Note 1. All GND(ground) pins should be connected together to the LCD module's metal frame.

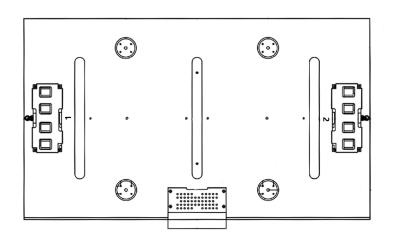
- 2. All VLCD (power input) pins should be connected together.
- 3. All Input levels of LVDS signals are based on the **EIA 644** Standard.
- 4. #1~#6 & #8~#10 NC (No Connection): These pins are used only for LGD (Do not connect)
- 5. Specific pin No. **#44** is used for "No signal detection" of system signal interface. It should be GND for NSB(No Signal Black) during the system interface signal is not. If this pin is "H", LCD Module displays AGP(Auto Generation Pattern).

3-2-2. Backlight Module

Table 3. ELECTRICAL CHARACTERISTICS (Continue)

narama	naramator		VAL	VALUES			Notes
parameter		Symbol	MIN	ТҮР	MAX		
Power supply inp	out voltage	VBL	23	24	25	VDC	1
Power supply inp	ut current	IBL_A		3.3		А	1500
Power consumpti	ion	PBL		72		W	cd/m ²
Power supply inp	ut current	IBL_A		4.2		А	2000
Power consumpti	ion	PBL		99		W	cd/m ²
Power supply inp	Power supply input current			5.4		А	2500
Power consumpti	ion	PBL		130		W	cd/m ²
Power supply inp	ut current	IBL_A		7.6		А	3000
Power consumpti	ion	PBL		180		W	cd/m ²
Input signal for	on	V on	2.5		5	V	
inverter control	off	V off	0		0.5	V	
Brightness adju	Brightness adjust		30		100	%	Automatic
							sensitization
							control

Rear view of LCM



Note: We may change it according to your actual needs. There may be no option 2

Product Specification

2、LED 恒流板接口

2.1 Board Α

P001 PH2.0-14PIN (2.0mm*14) P002 PH2.0-2PIN (2.0mm*2)

Pin	symbol	P001	P002	note
NO.		Description	Description	
1	VCC	Power Supply Voltage	Light sensor negative pole -	
2	VCC	Power Supply Voltage	Light sensor positive pole +	
3	VCC	Power Supply Voltage		
4	VCC	Power Supply Voltage		
5	VCC	Power Supply Voltage		
6	GND	Power ground		
7	GND	Power ground		
8	GND	Power ground		
9	GND	Power ground		
10	GND	Power ground		
11	NC	Not connect		
12	ON/OFF	Output enable signal		
13	NC	Not connect		
14	NC	Not connect		

2.2 Board B

P001 PH2.0-14PIN (2.0mm*14)

P002 PH2.0-2PIN (2.0mm*2)

Pin	symbol	P001	P002	note
NO.		Description	Description	
1	VCC	Power Supply Voltage	NC	
2	VCC	Power Supply Voltage	NC	
3	VCC	Power Supply Voltage		
4	VCC	Power Supply Voltage		
5	VCC	Power Supply Voltage		
6	GND	Power ground		
7	GND	Power ground		
8	GND	Power ground		
9	GND	Power ground		
10	GND	Power ground		
11	NC	Not connect		
12	NC	Not connect		
13	NC	Not connect		
14	NC	Not connect		

3-3. Signal Timing Specifications

Table 6 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timings should be satisfied with the following specification for normal operation.

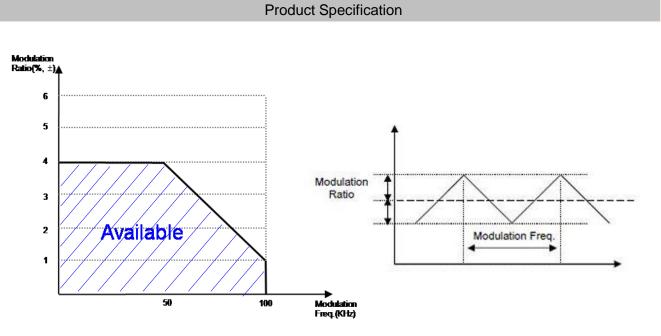
ITE	ITEM		Min	Тур	Мах	Unit	notes
	Display Period	tH∨	960	960	960	tCLK	1920 / 2
Horizontal	Blank	tнв	100	140	240	tCLK	1
	Total	tHP	1060	1100	1200	tCLK	
Vertical	Display Period	tvv	1080	1080	1080	Lines	
	Blank	tvв	20	45	300	Lines	1
	Total	tVP	1100	1125	1380	Lines	

Table 6. TIMING TABLE for NTSC & PAL(DE Only Mode)

ITE	ITEM		Symbol Min		Max	Unit	notes
	DCLK	fclk	60.00	74.25	78.00	MHz	
Frequency	Horizontal fн		57.3	67.5	70	KHz	2
	Vertical	fv	47	60	63	Hz	2

Note: 1. The input of HSYNC & VSYNC signal does not have an effect on normal operation (DE Only Mode). If you use spread spectrum of EMI, add some additional clock to minimum value for clock margin.

- 2. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate and the horizontal frequency
- Spread Spectrum Rate (SSR) for 50KHz ~ 100kHz Modulation Frequency(FMOD) is calculated by (7 – 0.06*Fmod), where Modulation Frequency (FMOD) unit is KHz.
 LVDS Receiver Spread spectrum Clock is defined as below figure
- * Timing should be set based on clock frequency.

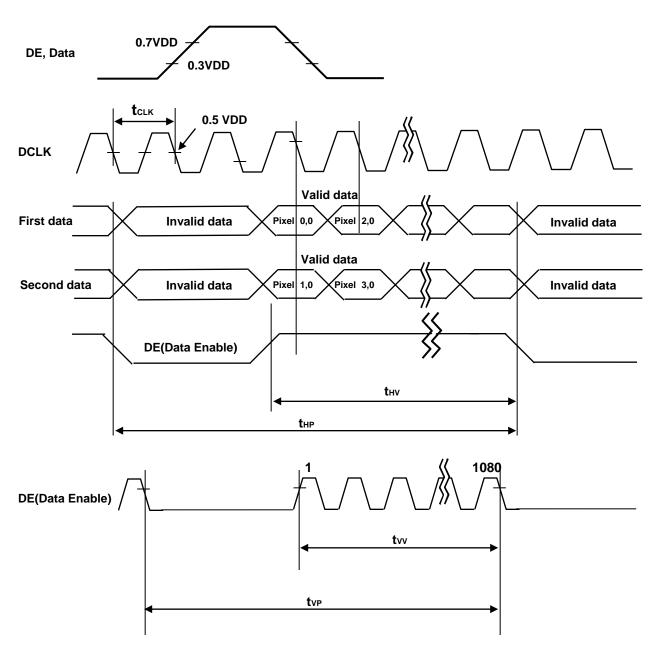


* Please pay attention to the followings when you set Spread Spectrum Rate(SSR) and Modulation Frequency(FMOD)

- 1. Please set proper Spread Spectrum Rate(SSR) and Modulation Frequency (FMOD) of TV system LVDS output.
- Please check FOS after you set Spread Spectrum Rate(SSR) and Modulation Frequency(FMOD) to avoid abnormal display. Especially, harmonic noise can appear when you use Spread Spectrum under FMOD 30 KHz.

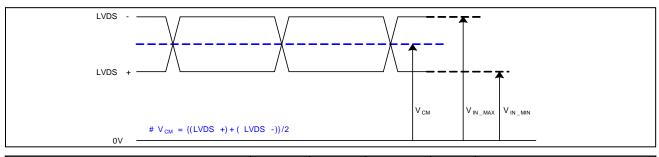
3-4. LVDS Signal Specification

3-4-1. LVDS Input Signal Timing Diagram



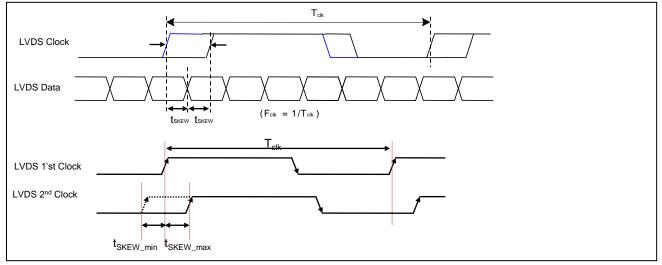
3-4-2. LVDS Input Signal Characteristics

1) DC Specification



Description	Symbol	Min	Max	Unit	notes
LVDS Common mode Voltage	V _{CM}	1.0	1.5	V	-
LVDS Input Voltage Range	V _{IN}	0.7	1.8	V	-
Change in common mode Voltage	ΔVCM	-	250	mV	-

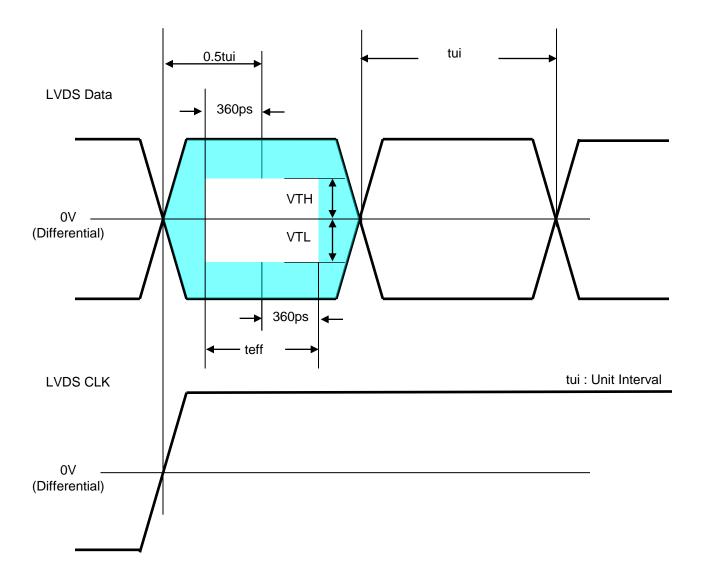
2) AC Specification



Description	Symbol	Min	Max	Unit	notes
LVDS Differential Valtage	V _{TH}	100	600	mV	Tested with Differential Probe
LVDS Differential Voltage	V _{TL}	-600	-100	mV	2
LVDS Clock to Data Skew	t _{skew}	-	(0.2*T _{clk})/7	ps	-
Effective time of LVDS	t _{eff}	±360	-	ps	-
LVDS Clock to Clock Skew (Even to Odd)	t _{SKEW_EO}	-	1/7* T _{clk}	ps	-

notes 1. All Input levels of LVDS signals are based on the EIA 644 Standard.

2. LVDS Differential Voltage is defined within t_{eff}



* This accumulated waveform is tested with differential probe

3-5. Color Data Reference

The brightness of each primary color(red,green,blue) is based on the 8bit gray scale data input for the color. The higher binary input, the brighter the color. Table 7 provides a reference for color versus data input.

Table 7.	COLOR DATA	REFERENCE
----------	------------	-----------

											I	npu	t Co	lor [Data										
Color		MS	SB		R	ED		L	SB	MS	SB		GRI	EEN	I	L	SB	MS	SB		BL	UE		L	SB
		R	7 R(6 R5	R4	R3	R2 F	R1 R	0	G	7 G6	G5	G4	G3	G2 (G1 (GO	в	7 B(6 B5	6 B4	В3	B2 I	31 E	80
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED					•	•															•				
	RED (254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
GREEN														••											
	GREEN (254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE		1			•••••									••											
	BLUE (254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

3-6. Power Sequence

3-6-1. LCD Driving circuit

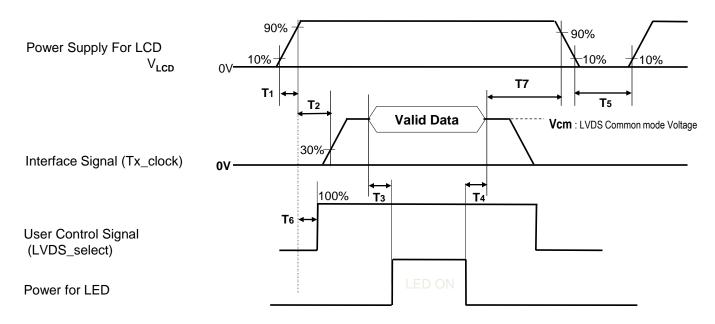


Table 8. POWER SEQUENCE

Devementer		Value						
Parameter	Min	Мах	Unit	Notes				
T1	0.5	-	20	ms	1			
T2	0	-	-	ms	2			
T3	400	-	-	ms	3			
T4	100	-	-	ms	3			
T5	1.0	-	-	S	4			
T6	0	-	T2	ms	5			
T7	0	-	-	ms	6			

Note :

1. Even though T1 is over the specified value, there is no problem if I2T spec of fuse is satisfied.

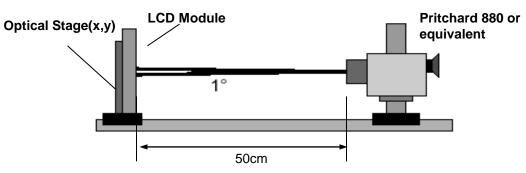
If T2 is satisfied with specification after removing LVDS Cable, there is no problem.
 The T3 / T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.

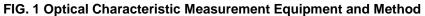
4. T5 should be measured after the Module has been fully discharged between power off and on period.

- 5. If the on time of signals (Interface signal and user control signals) precedes the on time of Power (V_{LCD}), it will be happened abnormal display. When T6 is NC status, T6 doesn't need to be measured.
- 6. It is recommendation specification that T7 has to be 0ms as a minimum value.
- * Please avoid floating state of interface signal at invalid period.
- * When the power supply for LCD (VLCD) is off, be sure to pull down the valid and invalid data to 0V.

4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable in a dark environment at $25\pm2^{\circ}$ C. The values are specified at 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0°. FIG. 1 shows additional information concerning the measurement equipment and method.





Ta= 25±2°C, V_{LCD}=12.0V, fv=60Hz, Dclk=74.25MHz, Duty =100%

				Value			
Pa	rameter	Symbol	Min	Тур	Max	Unit	notes
Contrast Ratio		CR	800	1100	-		1
Surface Luminar	nce, white	L _{WH}	0000	0000	-	cd/m ²	2
Luminance Varia	ition	δ _{WHITE} 9P	65	-	-		3
Response Time	Gray to Gray (BW)	G to G BW		12(TBD)	16(TBD)	ms	4
	RED	Rx		0.647(TBD)			
	RED	Ry		0.334(TBD)			
	GREEN	Gx		0.306(TBD)	Тур		_
Color Coordinate	-	Gy	-0.03	0.601(TBD)	+0.03		5
[CIE1931]	BLUE	Bx		0.153(TBD)			
	BLUE	Ву		0.052(TBD)			
		Wx	Тур	0.281(TBD)	Тур		_
	WHITE	Wy	-0.03	0.288(TBD)	+0.03		5
Color Temperatu	re			10,000		К	
Color Gamut				68		%	
Viewing Angle (0	CR>10)						
xa	xis, right(φ=0°)	θr	89	-	-		
xa	x axis, left (ϕ =180°)		89	-	-	dograd	6
y axis, up (_{\$=90°})		θu	89	-	-	degree	Ö
y a	y axis, down (∳=270°)		89	-	-		
Gray Scale			-	-	-		7

Table 10. OPTICAL CHARACTERISTICS

Note : 1. Contrast Ratio(CR) is defined mathematically as :

Contrast Ratio = Surface Luminance with all white pixels Surface Luminance with all black pixels

It is measured at center 1-point.

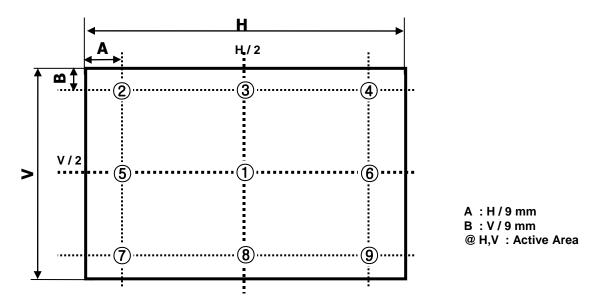
- Surface luminance are determined after the unit has been 'ON' and 1 Hour after lighting the backlight in a dark environment at 25±2°C. Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 3.
- 3. The variation in surface luminance , δ WHITE is defined as : δ WHITE(9P) = Minimum (Lon1,Lon2~ Lon8, Lon9) / Maximum (Lon1,Lon2~ Lon8, Lon9)*100 Where Lon1 to Lon9 are the luminance with all pixels displaying white at 9 locations . For more information, see the FIG. 3.
- 4. Response time is the time required for the display to transit from any gray to white (Rise Time, Tr_R) and from any gray to black (Decay time, Tr_D). For additional information see the FIG. 4.
 ※ G to G_{BW} Spec stands for average value of all measured points. Photo Detector : RD-80S / Field : 2 °
- 5. White, Red, Green, Blue Color Coordinates are measured at gray level 255(100IRE)
- 6. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 5.
- 7. Gray scale specification

Gamma Value is approximately 2.2. For more information, see the Table 11.

Product Specification

Gray Level	Luminance [%] (Typ)
LO	0.07(TBD)
L15	0.27
L31	1.04
L47	2.49
L63	4.68
L79	7.66
L95	11.5
L111	16.1
L127	21.6
L143	28.1
L159	35.4
L175	43.7
L191	53.0
L207	63.2
L223	74.5
L239	86.7
L255	100

Table 11. Gray scale specification



Measuring point for surface luminance & measuring point for luminance variation.

FIG. 3 9 Points for Luminance Measure

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Black or White".

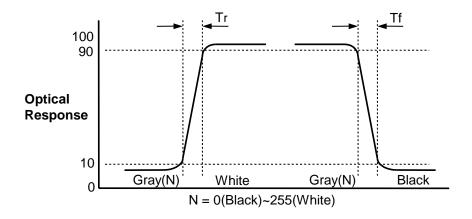
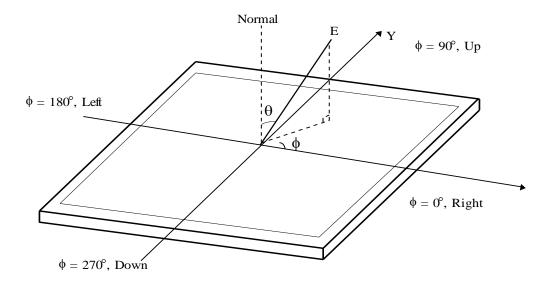


FIG. 4 Response Time

Dimension of viewing angle range





5. Mechanical Characteristics

Table 12 provides general mechanical characteristics.

ltem	Va	lue		
	Horizontal	973.8 mm		
Outline Dimension	Vertical	567.0 mm		
	Depth	58 mm		
Bezel Area	Horizontal	945.0 mm		
Dezel Alea	Vertical	533.0 mm		
Active Display Area	Horizontal	941.18 mm		
Active Display Area	Vertical	529.42 mm		
	Material	SUS Like PCM		
Case Top	Case Top Color	SHINE TITAN(HC583B)		
	LG Logo Color	SILVER (Pantone-877C)		
Weight	11.25kg (Typ.) 11.5kg(Max)			

Note : Please refer to a mechanic drawing in terms of tolerance at the next page.

6. Reliability

Table 13. ENVIRONMENT TEST CONDITION

No.	Test Item	Condition
1	High temperature storage test	Ta= 60°C 90% 240h
2	Low temperature storage test	Ta= -20°C 240h
3	High temperature operation test	Ta= 50°C 50%RH 500h
4	Low temperature operation test	Ta= 0°C 500h
5	Humidity condition Operation	Ta= 40 °C, 90%RH
6	Altitude operating storage / shipment	0 – 16,400 ft 0 - 40,000 ft
7	Vibration test (non-operating)	TBD
8	Shock test (non-operating)	TBD

Note : 1. Before and after Reliability test, LCM should be operated with normal function.

7. International Standards

7-1. Safety

- a) UL 60065, Underwriters Laboratories Inc. Audio, Video and Similar Electronic Apparatus - Safety Requirements.
- b) CAN/CSA C22.2 No.60065:03, Canadian Standards Association. Audio, Video and Similar Electronic Apparatus - Safety Requirements.
- c) EN 60065, European Committee for Electrotechnical Standardization (CENELEC). Audio, Video and Similar Electronic Apparatus - Safety Requirements.
- d) IEC 60065, The International Electrotechnical Commission (IEC). Audio, Video and Similar Electronic Apparatus - Safety Requirements.

7-2. Environment

a) RoHS, Directive 2011/65/EU of the European Parliament and of the council of 8 June 2011

9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

9-1. Mounting Precautions

- (1) You must mount a module using specified mounting holes (Details refer to the drawings).
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress, Concentrated stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.
- (10) Touching the LED Driver might cause an electric shock and damage to LED Driver. Please always use antistatic tools when handling the LED Driver

9-2. Operating Precautions

- (1) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (2) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer
- (3) Be careful for condensation at sudden temperature change.Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (4) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (5) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (6) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (7) A screw which is fastened up the steels should be a machine screw.
- (if not, it can causes conductive particles and deal LCM a fatal blow)
- (8) Please do not set LCD on its edge.
- (9) The conductive material and signal cables are kept away from LED driver inductor to prevent abnormal display, sound noise and temperature rising.

9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

It is recommended that they be stored in the container in which they were shipped.

- (3) Storage condition is guaranteed under packing conditions.
- (4) The phase transition of Liquid Crystal in the condition of the low or high storage temperature will be recovered when the LCD module returns to the normal condition.

9-6. Handling Precautions for Protection Film

- (1) The protection film is attached to the bezel with a small masking tape.
- When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ionblown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normalhexane.

9-7. Appropriate Condition for Commercial Display

- Generally large-sized LCD modules are designed for consumer applications (TV).

Accordingly, a long-term display like in Commercial Display application, can cause uneven display including image sticking. To optimize module's lifetime and function, several operating usages are required.

- 1. Normal operating condition
 - Temperature: 0 ~ 40 ℃
 - Operating Ambient Humidity : 10 ~ 90 %
 - Display pattern: dynamic pattern (Real display)

Note) Long-term static display can cause image sticking.

- 2. Operating usages under abnormal condition
- a. Ambient condition
 - Well-ventilated place is recommended to set up Commercial Display system.
- b. Power and screen save
- Periodical power-off or screen save is needed after long-term display.

APPENDIX- III-1

■ Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7= "L" or "NC")

Host System]	тнс	63LVD103]			
24 Bit		or C	ompatible				Timing
RED0		33					Controller
RED1		34		FI FI	-RE51S-	HF	
RED2		35					
RED3		36		31			
RED4		37	TA-		12	100Ω ≷	RO0N
RED5		38	TA+	30	13	10022	RO0P
RED6		59					
RED7		61	TB-	29	14		RO1N
		4	TB+	28	- 15	100Ω ≶	RO1P
		5	ID+		- 15	`	ROIP
GREEN0		40		25			
GREEN1		41	TC-	24	16	4000	RO2N
GREEN2		42	TC+		17	<u>100Ω </u>	RO2P
GREEN3		44					
GREEN4		45	TCLK-	23	19		ROCLKN
GREEN5		46	TCLK+	22	- 20	<u>100Ω </u>	ROCLKP
GREEN6		62	TOLKT		20		
GREEN7		63		21			
		6	TD-	20	22	4000	RO3N
		8	TD+	20	23	100Ω ξ	RO3P
BLUE0		48					
BLUE1		49					
BLUE2		50					
BLUE3		52					
BLUE4 BLUE5		53 54					
BLUE5 BLUE6		54 64			7		VESA/ JEIDA
BLUE0 BLUE7		1					
BLUE/		9					
		11			1		
Hsync		55					
Vsync		57		GND		LCM Module	
Data Enable		58		Ð	L		
CLOCK		12					
				J			

Note: 1. The LCD module uses a 100 $Ohm[\Omega]$ resistor between positive and negative lines of each receiver input.

- 2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)
- 3. '7' means MSB and '0' means LSB at R,G,B pixel data.

APPENDIX- III-2

■ Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7= "H")

Host System	тнс	63LVD103				
24 Bit	or Co	ompatible				Timing
RED0	4					Controller
RED1	5		FI-RE51S-HF			
RED2	59					
RED3	 61		31			
RED4	33	TA-		12	4000	RO0N
RED5	34	TA+	30	13	100Ω \$	RO0P
RED6	 35					
RED7	 36	TB-	29	14		RO1N
	 37	TB+	28		100Ω \$	RO1P
	 38	IB+		15		ROTP
GREEN0	6		25			
GREEN1	8	TC-		16		RO2N
GREEN2	62	TC+	24	17	<u>100Ω </u>	RO2P
GREEN3	63					
GREEN4	40	TCLK-	23	19		ROCLKN
GREEN5	41	TCLK+	22	20	100Ω \	ROCLKP
GREEN6	 42	ICLK+		20	、	RUCLAP
GREEN7	44		21			
	45	TD-		22	1000 2	RO3N
	46	TD+	20	23	<u>100Ω </u>	RO3P
BLUE0	 9					
BLUE1	 11					
BLUE2	64					
BLUE3	1					
BLUE4	 48 49					
BLUE5 BLUE6	49 50			7		VESA / JEIDA
BLUE6 BLUE7	 50 52					
BLUE/	52 53					
	54			1		
Hsync	55					
Vsync	57		VCC		LCM Module	
Data Enable	58		ö	L		
CLOCK	12					
	·		J			

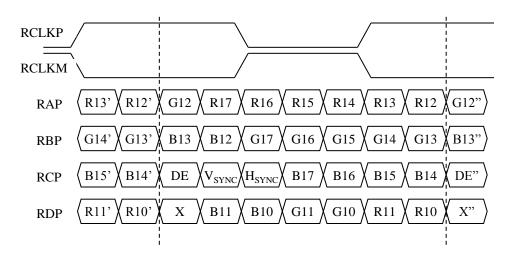
Note :1. The LCD module uses a 100 $Ohm[\Omega]$ resistor between positive and negative lines of each receiver input.

- 2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)
- 3. '7' means MSB and '0' means LSB at R,G,B pixel data.

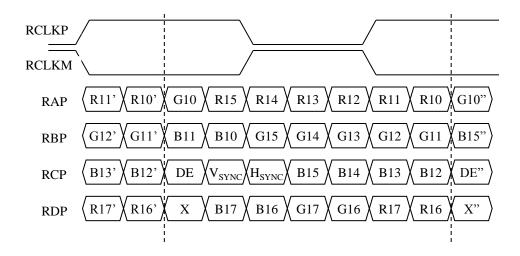
APPENDIX- IV

LVDS Data-Mapping Information (8 Bit)

1) LVDS Select : "H" Data-Mapping (JEIDA format)



2) LVDS Select : "L" Data-Mapping (VESA format)



APPENDIX- V

■ Option Pin Circuit Block Diagram

1) Circuit Block Diagram of LVDS Format Selection pin

